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{Control_phase-lock, author = {Ed Loops Control} Principles of Coherent Phase Locked Loop performance in the presence of CW interference and

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Phase Lock Loops: Theory and Practice Brennan, Paul V. (1) Describes the design of a phase locked loop

United states patent (10) patent n0.: us 7,042,971

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Real-time fringe pattern demodulation with a second-order P. Brennan, Phase Locked Loops: Principles and Phase Locked Loops: Principles and Practice

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P.V. Brennan, Phase-Locked Loops: Principles and Practice. MacMillan Press Ltd., Great Britain, 1996.

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Prof Paul Brennan. PhD, CEng, FIET. Professor of Microwave Electronics. Room 1110, Roberts Building, UCL. Phase-locked loops, principles and practice. UK: Macmillan.

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Patent us6417738 - adaptive frequency-hopping

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Recommended Practice for utility Interface Phase-Locked Loops P. V. Brennan, Phase-Locked Loops: Principles and Practice, Macmillan Press, New

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A phase-locked loop or phase lock loop In practice, one would likely insert other operations into the feedback of this phase-locked loop. For example,

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In practice, a capacitor CP is connected in parallel P. Brennan, Phase-Locked Loops, Principles and Practices, MacMillan Press, 1996

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